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Lerner
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Berhard Schätzler et al.
Div. of Applic. No. : 09/359,178, filed July 22, 1999
Div. filed : October 16, 2000
Title : Electronic Component with an Integrated Circuit Mounted on
an Island of a Lead Frame
Examiner : Christian Wilson Group Art Unit: 2824

INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner of Patents and Trademarks,
Washington, D.C. 20231

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are
cited herewith:

U.S. Patent No. 4,258,381 (Inaba), dated March 1981;

U.S. Patent No. 5,358,905 (Chiu), dated October 1994;

Published, European Patent Application No. 0 261 324 A1 (Bednarz et al.), dated March
1988;

Electronic Packaging and Interconnection Handbook, C.A. Harper, New York, pp.
1.40, 9.112, 9.157.

Above-mentioned references cited in an Office Action dated March 15, 2000, in parent
application No. 09/359,178.

Respectfully submitted,

For Applicants

Date: October 16, 2000

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